Language Processing Systems Overview of the Course

> Hitoshi Oi The University of Aizu

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AY2006, 2nd Semester

Basic Course Information

#927 Language Processing Systems

Instructor Hitoshi Oi, hitoshi@u-aizu.ac.jp, ext 2580

Office Hours TBD or by appointment, at 242-C

Textbook "Engineering a Compiler", by Keith D. Cooper and Linda Torczon, Published by Morgan Kaufmann.

Web site http://www.u-aizu.ac.jp/~hitoshi/COURSES/LP06/

Course Assistant Masato Kido, s1110071@u-aizu.ac.jp

Grading Schemes and Policies

Information in this slide is subject to change. Keep checking the course web site.

Grading Scheme

Final Grade = Midterm Exam (25%) + Final Exam (35%)+ Exercises (40%)

Late Submissions Exercise problems must be submitted by the end of the exercise class time in which the problems are given. A late submission is accepted by the end of next week's lecture with a penalty of 50% point reduction.

Exam Policies Only the textbook and handouts can be used during the exams.

Rough Course Schedule

Today Course Overview and Introduction to compilers.

Next Four to Five Weeks Scanner: dividing the source code into meaningful chunks (tokens).

Mid-term Exam

Next Four to Five Weeks Parser: building legal sentences using the grammar and tokens from the scanner.

Final Exam

As shown above, emphasis is placed on the front-end part of the compilers in this class. However, we also cover some topic related to the back-end of the compiler (such as register allocation).

Advices on Taking This Course

Textbook Get your own copy and read the corresponding sections (Only 27 students ordered so far).

Lecture Slides We will use the slides prepared by the author of the textbook. You will need to down load and make a hard copy of the slides corresponding to each lecture.

Class Attendance Come to class and ask questions (so you need to be prepared to ask questions). Attendance will be checked but will be used only for extra credit.

Use The Instructor Ask question during the class and the office hours as well.

About The Instructor

- Assistant Professor in the Operating Systems Laboratory
- Areas of research interests:
 - Computer architecture and operating systems
 - Performance evaluation and workload analysis
 - Microprocessor design
 - VLSI design and verification
- Students having interests in the above areas are welcome to join our group.
- We will start a study group from October 3 (every Tuesday) at 7pm.

Continue to the lecture slides...